

## **CLAIMS**

Please cancel claims 1-20 without prejudice or disclaimer, and please add new claims as shown in the following claim listing.

1-20. (Canceled).

21. (New) An apparatus comprising:

a first domino gate to control a signal at a first node based on a current input signal at an input node;

a second domino gate to control a signal at a second node based on a previous input signal at the input node; and

circuitry coupled to the first and second nodes to generate an encoded signal in response to a transition between a current input signal at the input node and a previous input signal at the input node.

22. (New) The apparatus of claim 21, wherein the circuitry is to generate an encoded signal indicative of transition between a current input signal at the input node and a previous input signal at the input node.

23. (New) The apparatus of claim 21, wherein the circuitry is to generate an encoded signal in response to no transition between a current input signal at the input node and a previous input signal at the input node.

24. (New) The apparatus of claim 21, wherein the circuitry is to generate an encoded signal independent of values corresponding to a current input signal and a previous input signal.

25. (New) The apparatus of claim 21, comprising a storage device to store a signal corresponding to a previous input signal at the input node.

26. (New) The apparatus of claim 25, wherein the storage device is to receive and store a signal corresponding to a signal at the first node.
27. (New) The apparatus of claim 21, wherein the first domino gate includes a transistor to be controlled based on a current input signal at the input node.
28. (New) The apparatus of claim 21, wherein the second domino gate includes a transistor to be controlled based on a previous input signal at the input node.
29. (New) The apparatus of claim 21, wherein the circuitry includes a first transistor to be controlled based on a signal at the first node and a second transistor to be controlled based on a signal at the second node.
30. (New) The apparatus of claim 21, wherein the circuitry is to transmit on a line a signal corresponding to an encoded signal.
31. (New) The apparatus of claim 21, comprising decode circuitry to decode an encoded signal to generate a decoded signal.
32. (New) The apparatus of claim 31, wherein the decode circuitry is to decode an encoded signal based on the encoded signal and a previous decoded signal.
33. (New) The apparatus of claim 31, wherein the decode circuitry includes a storage device to receive and store a signal corresponding to a previous decoded signal.
34. (New) The apparatus of claim 31, wherein the decode circuitry includes a first transistor to be controlled based on an encoded signal and a second transistor to be controlled based on a previous decoded signal.

35. (New) A method comprising:

controlling a signal at a first node by a first domino gate based on a current input signal at an input node;

controlling a signal at a second node by a second domino gate based on a previous input signal at the input node; and

generating an encoded signal based on signals at the first and second nodes in response to a transition between a current input signal at the input node and a previous input signal at the input node.

36. (New) The method of claim 35, wherein the generating an encoded signal includes generating an encoded signal indicative of transition between a current input signal at the input node and a previous input signal at the input node.

37. (New) The method of claim 35, comprising decoding an encoded signal.

38. (New) A system comprising:

a line;

an encoder circuit coupled to the line, the encoder circuit including a first domino gate to control a signal at a first node based on a current input signal at an input node, a second domino gate to control a signal at a second node based on a previous input signal at the input node, and circuitry coupled to the first and second nodes to generate an encoded signal in response to a transition between a current input signal at the input node and a previous input signal at the input node, the circuitry to transmit on the line a signal corresponding to an encoded signal; and

a decoder circuit coupled to the line to decode an encoded signal.

39. (New) The system of claim 38, wherein the circuitry is to generate an encoded signal indicative of transition between a current input signal at the input node and a previous input signal at the input node.

40. (New) The system of claim 38, wherein the decoder circuit is to decode an encoded signal based on the encoded signal and a previous decoded signal.